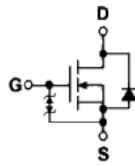


FBP1006



Symbol



Parameter	Value	Unit
V_{DS}	60	V
$R_{DS(ON)-Max}$	1.9	Ω
I_D	0.4	A

Key Features

- Fast switching speed
- Reliable and Rugged
- ROHS Compliant & Halogen-Free
- ESD Protection

Applications

- Small signal application
- Load switch

Ordering Information

Ordering part Number	Marking code	Package	Form
VPLMDF7025	7025	FBP1006	Tape & Reel

Absolute Maximum Ratings (T_j = 25°C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Maximum Junction Temperature	T_J	150	$^{\circ}C$
Storage Temperature Range	T_{STG}	-55 to 150	$^{\circ}C$
Pulse Drain Current Tested	$T_A=25^{\circ}C$ $I_{DM}^{(1)}$	1.1	A
Continuous Drain Current	$T_A=25^{\circ}C$	0.4	A
	$T_A=70^{\circ}C$	0.3	
Maximum Power Dissipation	$T_A=25^{\circ}C$	0.7	W
	$T_A=70^{\circ}C$	0.4	

Thermal Characteristics

Parameter	Symbol	Rating	Unit
Thermal Resistance-Junction to Ambient	Steady State $R_{\theta JA}^{(3)}$	180	$^{\circ}C/W$

- (1) Max. current is limited by junction temperature.
- (2) UIS tested and pulse width are limited by maximum junction temperature 150°C
- (3) Surface Mounted on 1in² FR-4 board with 1oz.

Electrical Characteristics ($T_j = 25^\circ\text{C}$, unless otherwise specified)

Static Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	60	-	-	V	$V_{GS}=0V, I_{DS}=250\mu A$
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	μA	$V_{DS}=48V, V_{GS}=0V$
Gate Threshold Voltage	$V_{GS(th)}$	1	1.6	2.5	V	$V_{DS}=V_{GS}, I_{DS}=250\mu A$
Gate Leakage Current	I_{GSS}	-	-	± 10	μA	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-Source On-state Resistance	$R_{DS(ON)}^{(4)}$	-	1.6	1.9	Ω	$V_{GS}=10V, I_{DS}=0.3A$
		-	1.7	2.2		$V_{GS}=4.5V, I_{DS}=0.2A$
Forward Transconductance	g_{fs}	-	0.45	-	S	$V_{DS}=10V, I_{DS}=0.2A$

Dynamic Characteristics⁽⁵⁾

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Input Capacitance	C_{iss}	-	26	-	pF	$V_{GS}=0V, V_{DS}=30V,$ $Freq.=1MHz$
Output Capacitance	C_{oss}	-	2.7	-		
Reverse Transfer Capacitance	C_{rss}	-	1.7	-		
Turn-on Delay Time	$t_{d(ON)}$	-	1	-	nS	$V_{GS}=10V, V_{DS}=30V,$ $I_D=0.3A, R_{GEN}=10\Omega$
Turn-on Rise Time	t_r	-	19.4	-		
Turn-off Delay Time	$t_{d(OFF)}$	-	23.2	-		
Turn-off Fall Time	t_f	-	21	-		

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Total Gate Charge	Q_g	-	0.9	-	nC	$V_{GS}=4.5V, V_{DS}=30V$ $I_D=1A$
		-	1.7	-		$V_{GS}=10V, V_{DS}=30V,$ $I_D=1A$
Gate-Source Charge	Q_{gs}	-	0.4	-		
Gate-Drain Charge	Q_{gd}	-	0.3	-		

Source Drain Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Diode Forward Voltage	$V_{SD}^{(4)}$	-	0.8	1.1	V	$I_{SD}=0.2A, V_{GS}=0V$
Reverse Recovery Time	t_{rr}	-	7.4	-	nS	$I_F=0.1A, V_R=10V$ & $di_F/dt=100A/\mu s$
Reverse Recovery Charge	Q_{rr}	-	2.3	-	nC	

- ⁽⁴⁾ Pulse test (pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$).
- ⁽⁵⁾ Guaranteed by design, not subject to production testing.

Electrical Characteristic Diagrams

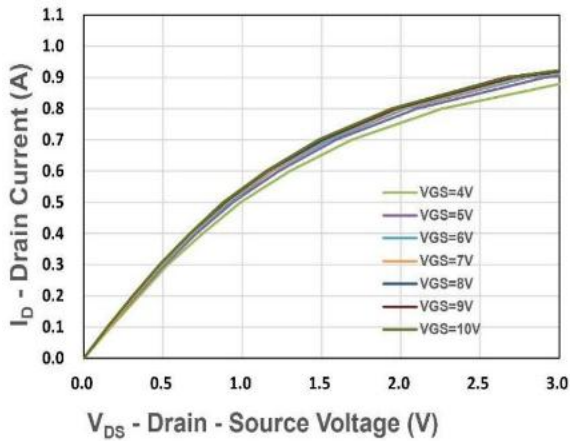


Figure 1. Output Characteristics

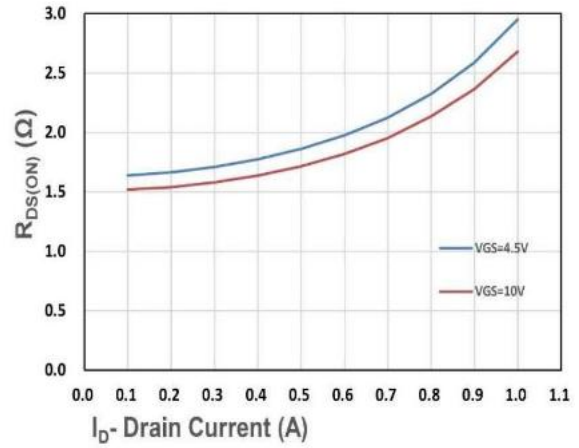


Figure 2. On-Resistance vs. ID

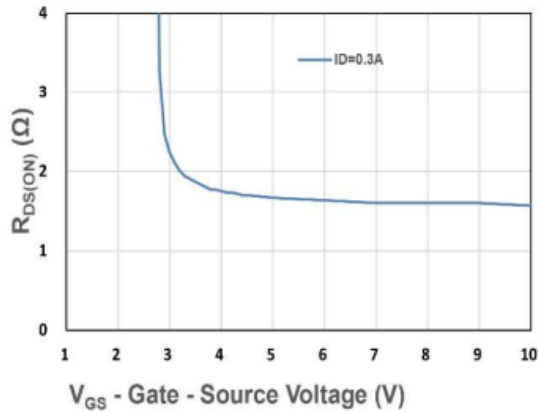


Figure 3. On-Resistance vs. VGS

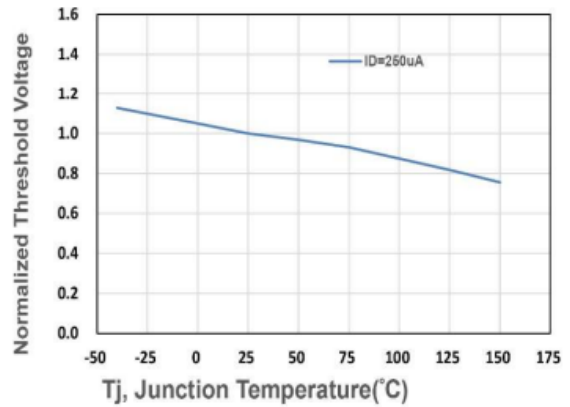


Figure 4. Gate Threshold Voltage

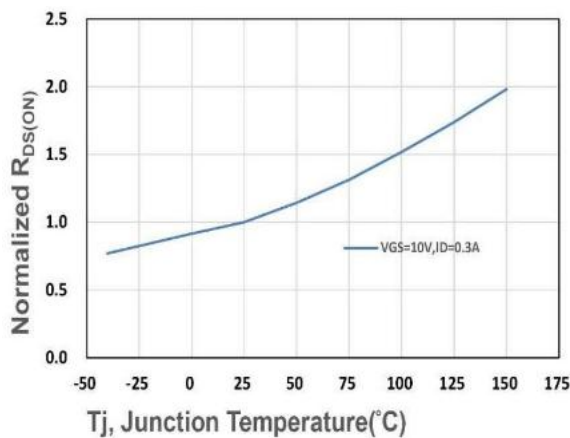


Figure 5. Drain-Source On Resistance

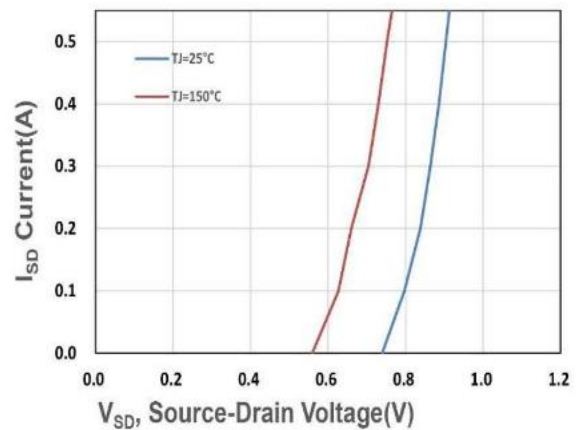


Figure 6. Source-Drain Diode Forward

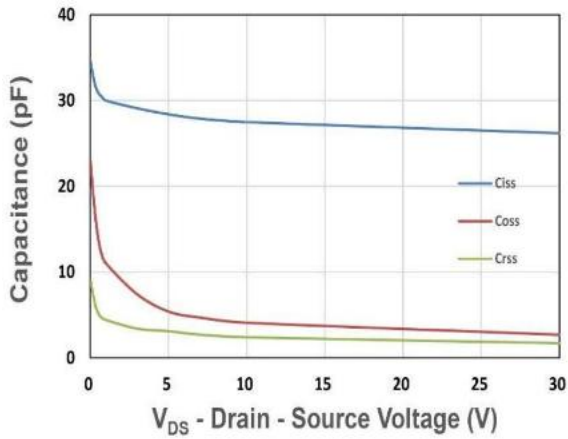


Figure 7. Capacitance

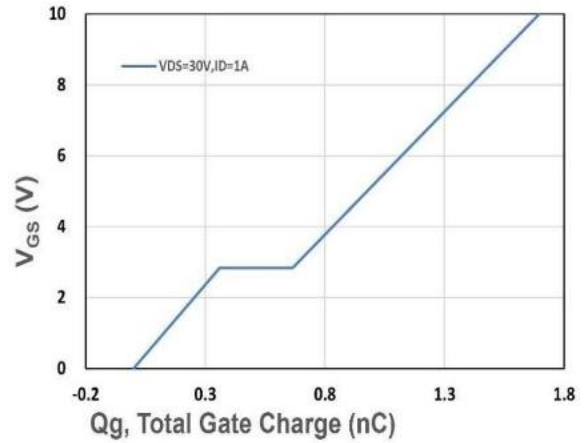


Figure 8. Gate Charge Characteristics

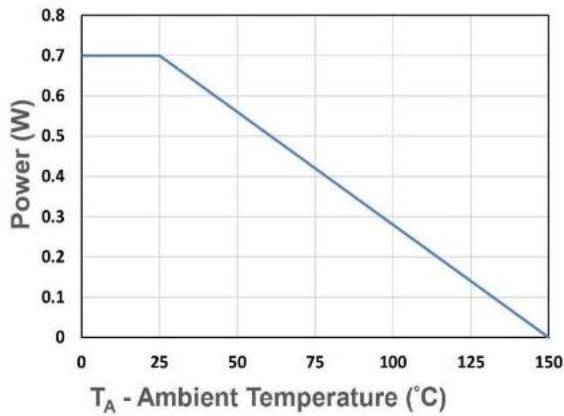


Figure 9. Power Dissipation

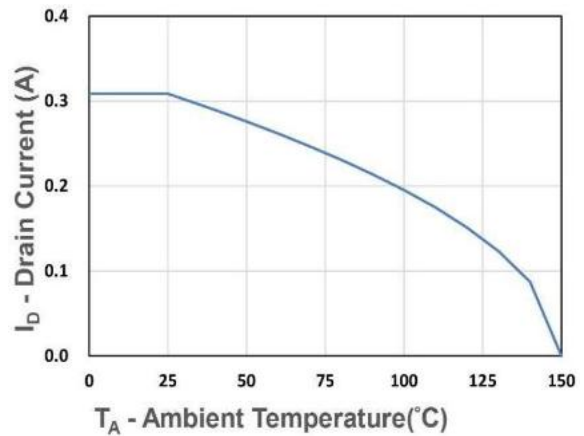


Figure 10. Drain Current

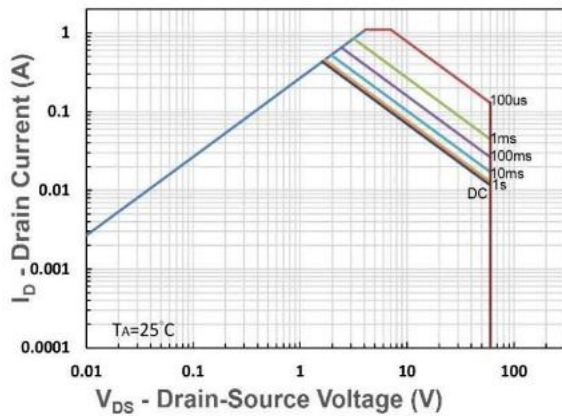


Figure 11. Safe Operating Area

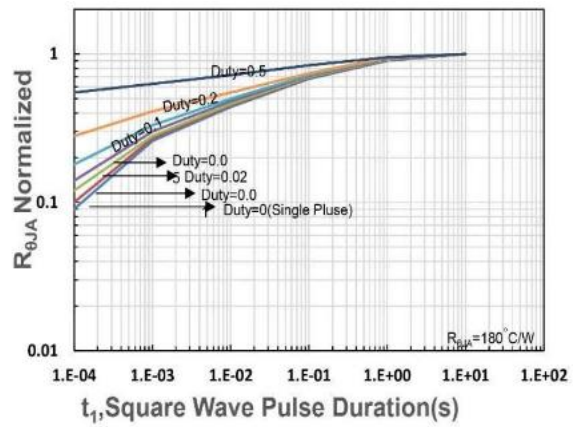
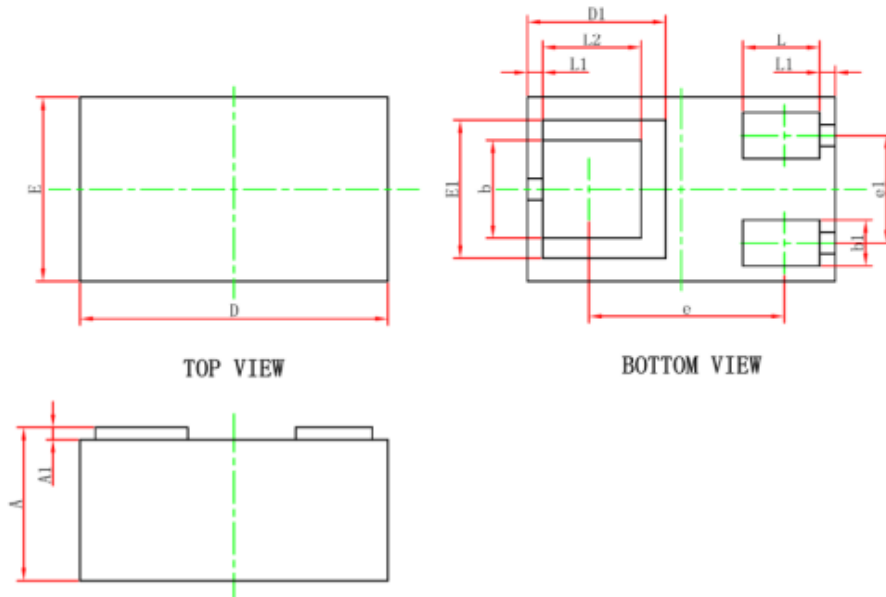


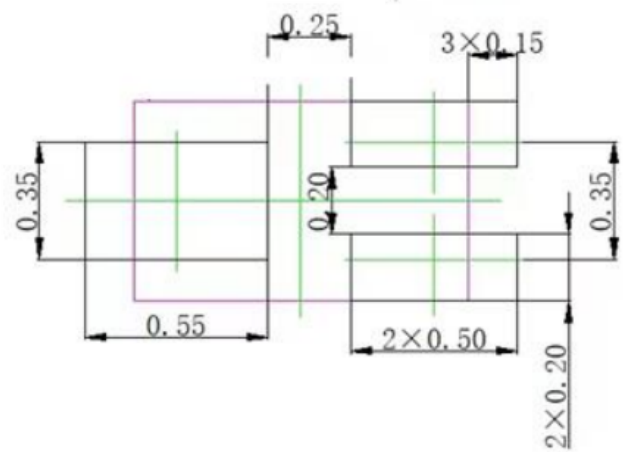
Figure 12. $R_{\theta JA}$ Transient Thermal Impedance

Package Information (FBP1006)



Outline dimensions in mm

Unit:mm	Min	Typ	Max
A	0.450	/	0.550
A1	0.010	/	0.100
D	0.950	/	1.050
E	0.550	/	0.650
D1	/	0.45 REF.	/
E1	/	0.45 REF.	/
b	0.270	/	0.370
b1	0.100	/	0.200
e	/	0.635 REF.	/
e1	0.300	/	0.400
L	0.200	/	0.300
L1	/	0.050 REF.	/
L2	0.270	/	0.370



Suggested Pad Layout (Unit:mm)

Land Pattern (Only for Reference)

Disclaimer

The information provided in this datasheet is believed to be accurate and reliable. Errors or omissions are expected. indiaVP Semiconductor Pvt. Ltd. reserves the right to make changes to the product specifications without prior notice. Users should verify the suitability of the product for their specific applications. Please visit our website for the latest datasheet.

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